Interference Search History Printout MT

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(NAND adj flash adj memory and stacked near gate and control near gate and floating near gate$1 and select near gate near dielectric and concave and tunnel near oxide). CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/09/01 10:52
L2	1	(NAND adj flash adj memory and stacked near gate and gate and floating near gate$1 and dielectric and concave and tunnel near oxide).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/09/01 10:52